

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	562	(712/244).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/23 18:27
L2	1	L1 and "time performance constraint"	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L3	85193	"DSP" or "digital signal processor"	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L4	66719	"DSP" or "digital signal processor" and detector	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L5	59910	"DSP" or "digital signal processor" and (detector near4 event)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L6	59759	"DSP" or "digital signal processor" and (detector near4 event) and 712/1-250	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L7	538	"digital signal processor" and (detector near4 event)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L8	302	((("DSP" or "digital signal processor") same memory) and (detector near4 event)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L9	33544	((("DSP" or "digital signal processor") same memory)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L10	292	((("DSP" or "digital signal processor") same memory same layout)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L11	2	("DSP" or "digital signal processor") and (memory and alu and register and "IO device") and (controller same event\$1) and detector	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L12	2933	("DSP" or "digital signal processor") and (memory and alu and register)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L13	568	("DSP" or "digital signal processor") same (memory and alu and register)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L14	17	("DSP" or "digital signal processor") and (memory and alu and register and "IO device")	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L15	123	("DSP" or "digital signal processor") same (memory and alu and register) and detector	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L16	37	("DSP" or "digital signal processor") same (memory and alu and register and ("IO device" or "I/O device" or "input/output")) and detector	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27

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L17	129	("DSP" or "digital signal processor") same (memory and alu and register and ("IO device" or "I/O device" or "input/output"))	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L18	88	((("DSP" or "digital signal processor") same (memory and alu and register and ("IO device" or "I/O device" or "input/output")))) and (interrupt or event or exception)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L19	3	"vector memory" same granularity	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L20	3358	memory same granularity	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L21	953	(memory same granularity) and resolution	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L22	1	("6292887").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/23 18:27
L23	1	"5216613".PN.	USPAT; USOCR	AND	ON	2006/10/23 18:27
L24	1	"3928857".PN.	USPAT; USOCR	AND	ON	2006/10/23 18:27
L25	1	"4445177".PN.	USPAT; USOCR	AND	ON	2006/10/23 18:27
L26	4	("3928857"   "4445177"   "5216613").PN. OR ("6292887").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L27	25247	granularity	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L28	1699	(granularity with memory)	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L29	195	(granularity with memory) same (high or low)	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L30	5	(granularity with memory) same (high or low) same delay	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L31	97	(granularity with memory) same (high or low) and delay	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L32	6	(granularity with memory) same (high or low) same event	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27

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L33	9	(granularity with memory) same (high or low) and (granularity same delay)	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L34	2	((("5982238") or ("6425064"))).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/23 18:27
L35	14	((granularity with memory) same split)	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L36	5329	morrison.in. and @py<"2003"	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L37	190	morrison.in. and @py<"2003" and processor	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L38	93	morrison.in. and (fir\$3 adj time)	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L39	1	(link\$2 adj list) and (timed near2 untimed near2 command)	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L40	1261	((high or low) adj resolution) with clock	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L41	43	((high or low) adj resolution) with clock and granularity	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L42	48	((high or low) adj resolution) with (clock or counter) and granularity	US-PGPUB; USPAT; USOCR	AND	ON	2006/10/23 18:27
L43	198	"firing time" with delay	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L44	47	("firing time" with delay) and (resolution or granularity)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L45	170	((("firing time" or deadline) with delay) and (resolution or granularity))	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L46	516	(multiple adj (clock or counter)) with (delay or deadline)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L47	113	(multiple adj (clock or counter)) with (delay or deadline) and (DSP or "real time")	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L48	154	((multiple or precis\$4) adj (clock or counter)) with (delay or deadline) and (DSP or "real time")	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L49	2	("firing time" with granularity)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27

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L50	597	(713/401).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/23 18:27
L51	745	(713/501).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/10/23 18:27
L52	81	(L50 or L51) and dsp	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L53	1944	(delay\$3 near2 resolution)	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L54	3	(delay\$3 near2 resolution) same (dsp or "digital signal processor")	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L55	186	(delay\$3 near2 resolution) same memory	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L56	68	(delay\$3 near2 resolution) with counter	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L57	640	"delay field"	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L58	0	L57 with split	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L59	3	L57 same split	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L60	2	L57 same (dsp or "digital signal processor")	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27
L61	80	L57 and (dsp or "digital signal processor")	US-PGPUB; USPAT	AND	ON	2006/10/23 18:27